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186W	AA1	6,58	3,747 B1	06/2003	van der Goes et al.	341	156	
	AB1	6,62	3,224 B1	09/2003	Mulder et al.	341.	156	
	AC1	J	8,898 B1	02/2003	Choksi	341	118	100
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	AE1	<u> </u>	9,913.	12/2002	Hansen et al.	341	156	
	AF1		9,502	01/2001	Johnson et al.	341	120	
• • •	AG1		9,745 B1	07/2001	Chan	375	285	±
	AH1		7,116	02/1999	Nakamura et al.	341	159	, i
	Al1		3,632	10/1999	Tai	341	156	
	AJ1		4,943	09/1996	Moreland	327	065	_
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	AB2	5,302,869	04/1994	Hosotani et al.	7)7	075	
	AC2	5,191,336	03/1993	Stephenson	341	111	<u> </u>
	AD2	5,118,971	06/1992	Schenck	726	072	1
	AE2	5,157,397	10/1992	Vernon	341	159	
	AF2	5,006,727	04/1991	Ragosch et al.	7)7	306	
	AG2	4,959,563	09/1990	Schenck	726	627	* **
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	AS	Brandt, B.I at Nyquist, 2 1795.	P. and Lutsky, J., " IEEE Journal of	"A 75-mW, 10-b, 20-MSPS C Solid-State Circuits, IEEE, Vo	MOS Subrano ol. 34, No. 12,	ging ADC with 9 December 199	.5 Effective Bits 9, pages 1788-
	AT	Bult, K. an of Solid-St	d Buchwald, A., ", ate Circuits, IEEE	An Embedded 240-mW 10-b , Vol. 32, No. 12, December	50-MS/s CMO 1997, pages 1	S ADC in 1-mm 887-1895.	² ," IEEE Journa
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		· 'l'	Choe, MJ. Trimming," / 1790.	et al., "A 13-b 40 EEE Journal of S	-Msample/s CMOS Pipeli colid-State Circuits, IEEE,	ned Folding ADC Vol. 35, No. 12, [with Backgroun December 2000,	d Offset pages 1781-
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Ingino, J. and Wooley, B., "A Continuously Calibrated 12-b, 10-MS/s, 3.3-V A/D Converter," *IEEE Journal of Solid-State Circuits*, IEEE, Vol. 33, No. 12, December 1998, pages 1920-1931. AS Ito, M. et al., "A 10 bit 20 MS/s 3 V Supply CMOS A/D Converter," IEEE Journal of Solid-State Circuits, IEEE, Vol. 29, No. 12, December 1994, pages 1531-1536. AT

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1		Kusumoto	, K. et al., "A 10-b	20-MHz 30-mW Pipelined Inter	polating CM	OS ADC," <i>IEEE</i>	Journal of
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Wang, Y-T. and Razavi, B., "An 8-Bit 150-MHz CMOS A/D Converter," *IEEE Journal of Solid-State Circuits*, IEEE, Vol. 35, No. 3, March 2000, pages 308-317.

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Page 11 of 11